ENTRY FROM
LOGIC YM
CONSOLE LOG
ERROR LOG
DIAGNOSE LOG

XX — 0A00 SCAN C
A SCAN-BYPASS
A →T
D SCANOUT-LTWD
L W*24
S STOP2
A2— XX —AB
SCANOUT LEFT ROLLER TO T REG. PUT IN LSWR.

XX — 0A01 SCAN C
S MS-REQ*SCAN4
A3— XX —AC

XX — 0A02 SCAN C
A SCAN-BYPASS
A →T
D SCANOUT-SREG
L R*24→S
S SET-MARK-0-7
R 0
A4— X0 —AD
SCANOUT S REG. TO T REG. PUT LSWR IN S.

X0 — 0A06 SCAN C
S STOP1
C ADRSQNCR-1
A5— XX —AE

XX — 0A03 SCAN C
A SCAN-BYPASS
A →T
D SCANOUT-LTWD
L W*24
S STOP1
A6— XX —AF
SCAN OUT LEFT ROLLER TO T REG. PUT IN LSWR.

XX — 0A1C SCAN C
A SCAN-BYPASS
A →T
D SCANOUT-RTWD
L R*24→S
S STOP1
A7— XX —AG
SCAN OUT RIGHT ROLLER TO T REG. PUT LSWR IN S.

XX — 0A1D SCAN C
S MS-REQ*SCAN4
A8— XX —AH

XX — 0A06 SCAN C
S SET-MARK-0-7
R SAS=0
A9— X* —AI

•SCAN — 0019
L R*24→S
C 1→SCAN-MODE
C1— XX —CA
READ LSWR TO S

X1 — 0A05 SCAN C
C Δ23→ADR-SQCR
R
E1— XX —EA

XX — 0A07 SCAN C
S MS-REQ*SCAN4
E2— XX —EB
FETCH ORIGINAL CONTENTS OF ST INTO AB TO ASSIGN GOOD PARITY.

XX — 0A08 SCAN C
E3— XX —EC
NULL

XX — 0A09 SCAN C
D MS→AB
S STOP2
C Δ7→ADR-SQCR
E4— XX —ED
DATA TO AB

XX — 0A0A SCAN C
S STOP2
E5— XX —EE
WAIT FOR AB TO BECOME STABLE.

XX — 0A0B SCAN C
A SCAN-BYPASS
A →T
D SCANOUT-LTWD
L W*24
S STOP1
E6— XX —EF
SCANOUT A TO T. PUT IN LSWR.

XX — 0A0C SCAN C
A SCAN-BYPASS
A →T
D SCANOUT-RTWD
L R*24→S
S STOP1
E7— XX —EG
SCANOUT B TO T. PUT LSWR IN S.

XX — 0A0D SCAN C
C Δ23→ADR-SQCR
E8— XX —EH

XX — 0A0E SCAN C
S MS-REQ*SCAN4
E9— XX —EI
STORE ORIGINAL CONTENTS OF ST WITH GOOD PARITY.

XX — 0A0F SCAN C
S SET-MARK-0-7
R 0
J1— X0 —JA

X0 — 0A10 SCAN C
S STOP1
C ADRSQNCR-1
J2— XX —JB

XX — 0A1E SCAN C
A SCAN-BYPASS
A →T
D SCANOUT-LTWD
L W*24
S STOP1
J3— XX —JC
SCANOUT LEFT ROLLER TO T REG. PUT IN LSWR.

XX — 0A12 SCAN C
L R*24→S
S MS-REQ*SCAN4
J4— XX —JD
PUT LSWR IN S REG AND STORE THAT WORD.

XX — 0A13 SCAN C
S SET-MARK-0-3
R Δ5AS=18
J5— X* —JE

X1 — 0A11 SCAN C
C Δ7→ADR-SQCR
R 0
J6— X0 —JF

X0 — 0A14 SCAN C
C ADRSQNCR-1
J7— XX —JG

XX — 0A16 SCAN C
J8— XX —JH

XX — 0A17 SCAN C
R SAS=0
J9— X* —JI
STEP ADDRESS SEQUENCER TO ZERO.

X1 — 0A15 SCAN C
A SCAN-BYPASS
A →T
D SCANOUT-RTWD
S STOP1
N2— XX —NB
ADDR SEQ EQ 0. SCANOUT ORIGINAL ST PARITY BITS FROM AB TO T.

XX — 0A18 SCAN C
A =1
S MS-REQ*SCAN4
N3— XX —NC
STORE ORIGINAL ST PARITY BITS.

XX — 0A19 SCAN C
A 0+Δ32
A →A
S SET-MARK*J61
N4— XX —ND
DEVELOP MCH CHK OLD PSW ADDR IN A.

XX — 0A1A SCAN C
A A+Δ32
A →A
S STOP1
C ΔMACH-RESET
N5— XX —NE

XX — 0A1B SCAN C
A A+0-16
A →D
C 3→MCH-CK-TRP
C 0→SCAN-MODE
N6— XX —NF
SET MACH CHK OR STOP TGR. ADDR 30 HEX TO D.

QU001 — NFE
(XX)TO MACHINE CHK INTERRUPT ROUTINE.

•DKEY — 05B0
A -1
A →T
L W*24
Q4— XX —QD

XX — 05B1
A 8+D
A →D
L R*24→S
S MS-REQ*D-3
Q5— XX —QE
STORE DATA KEYS AT MCW+8 ADDRESS.

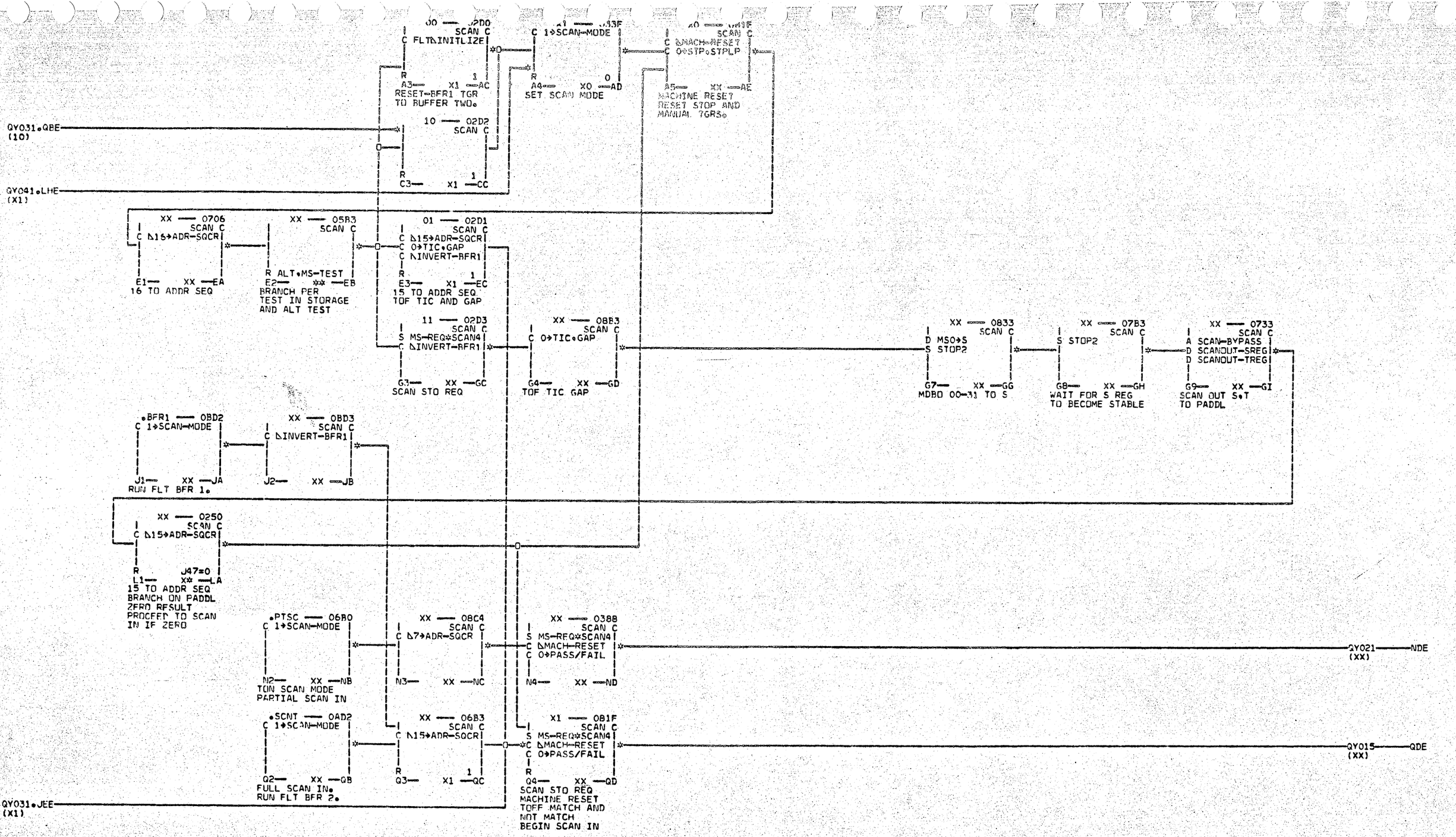
XX — 05B7
D DATA-KEYS→ST
S SET-MARK-0-7
Q6— XX —QF

QT041 — QFE
(XX)TO END OP.

STORE DATA KEYS
ENTRY IS FROM
DIAGNOSE INST.
QQ171.CH

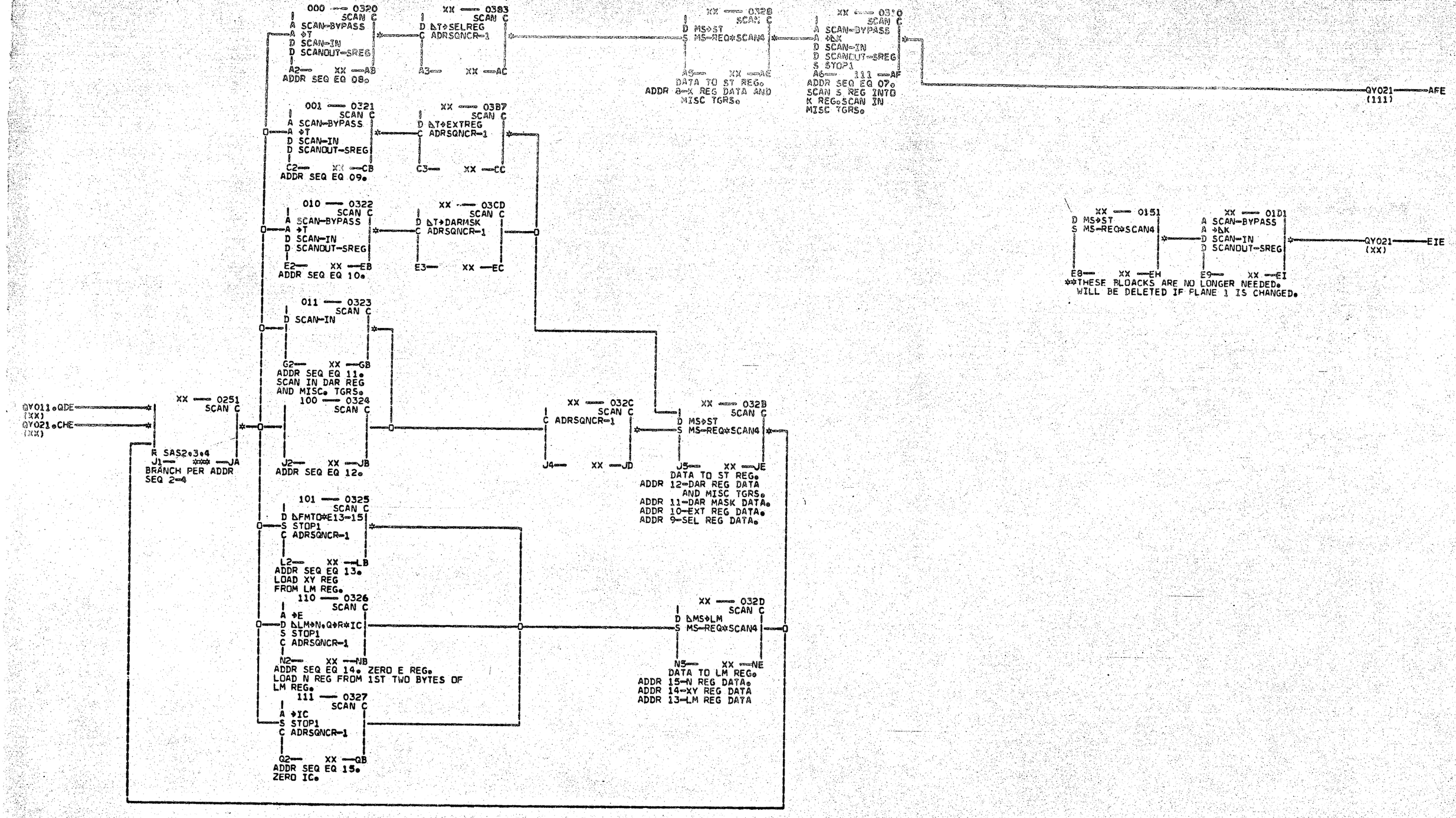
QY001

713671	04/28/70	MACH	C7201-02	DATE 05/21/70	SHEET 1 QY001
		NAME		LOG 048	VERSION
		MODE	MANUAL	ROS CONTROLLED LOG OUT	
		P.N.	2583896	STORE CONSOLE DATA KEYS	
		IBM CORP.	SDD		

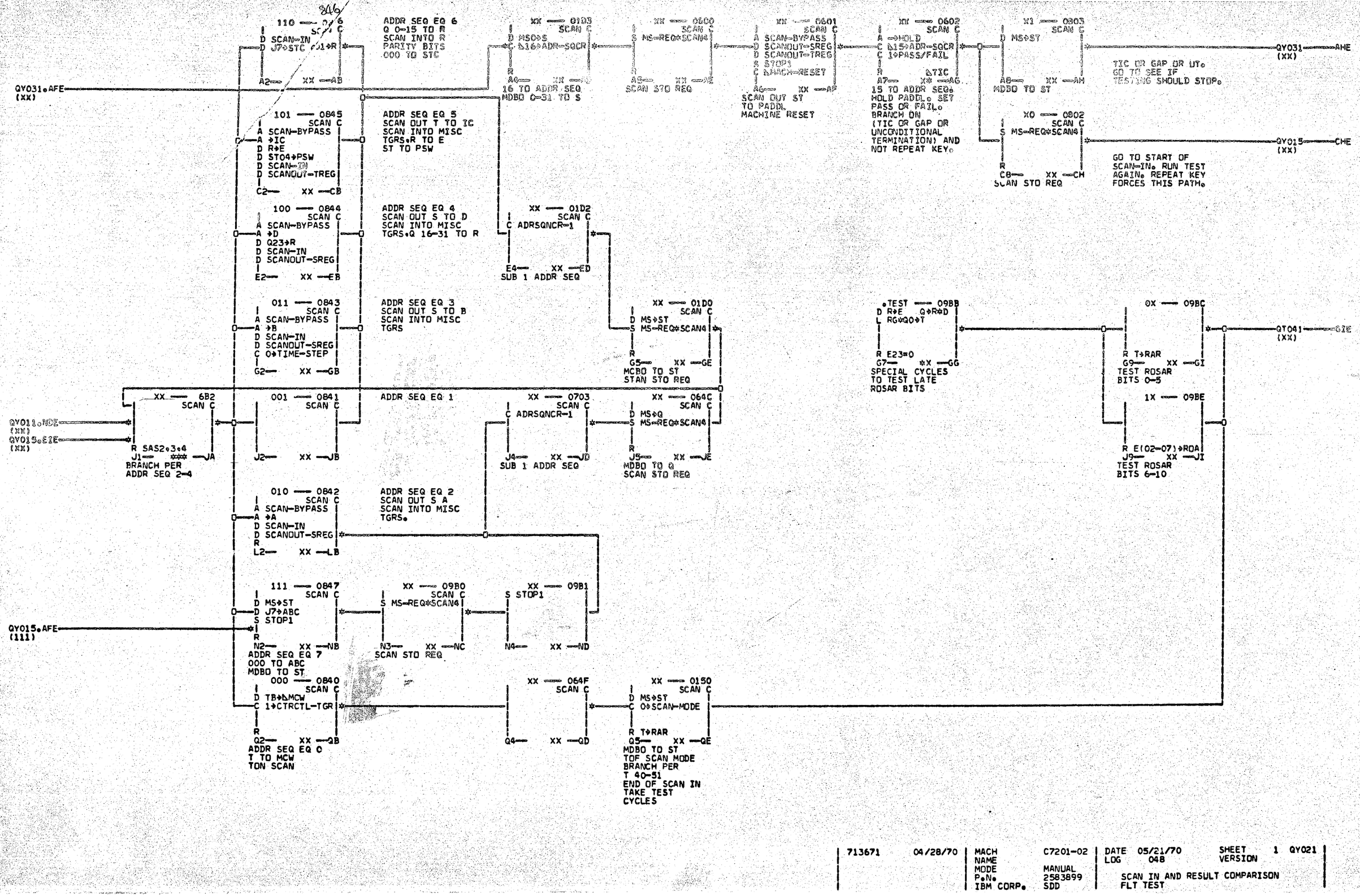


713671	04/28/70	MACH	C7201-02	DATE 09/04/70	SHEET 1 QY011
714949	08/14/70	NAME		LOG 334	.VERSION
		MODE	MANUAL		
		P.N.	2583897	WAITING STATE AND TEST NUMBER	
		IBM CORP.	SDD	COMPARISON FLT TEST	

QY011

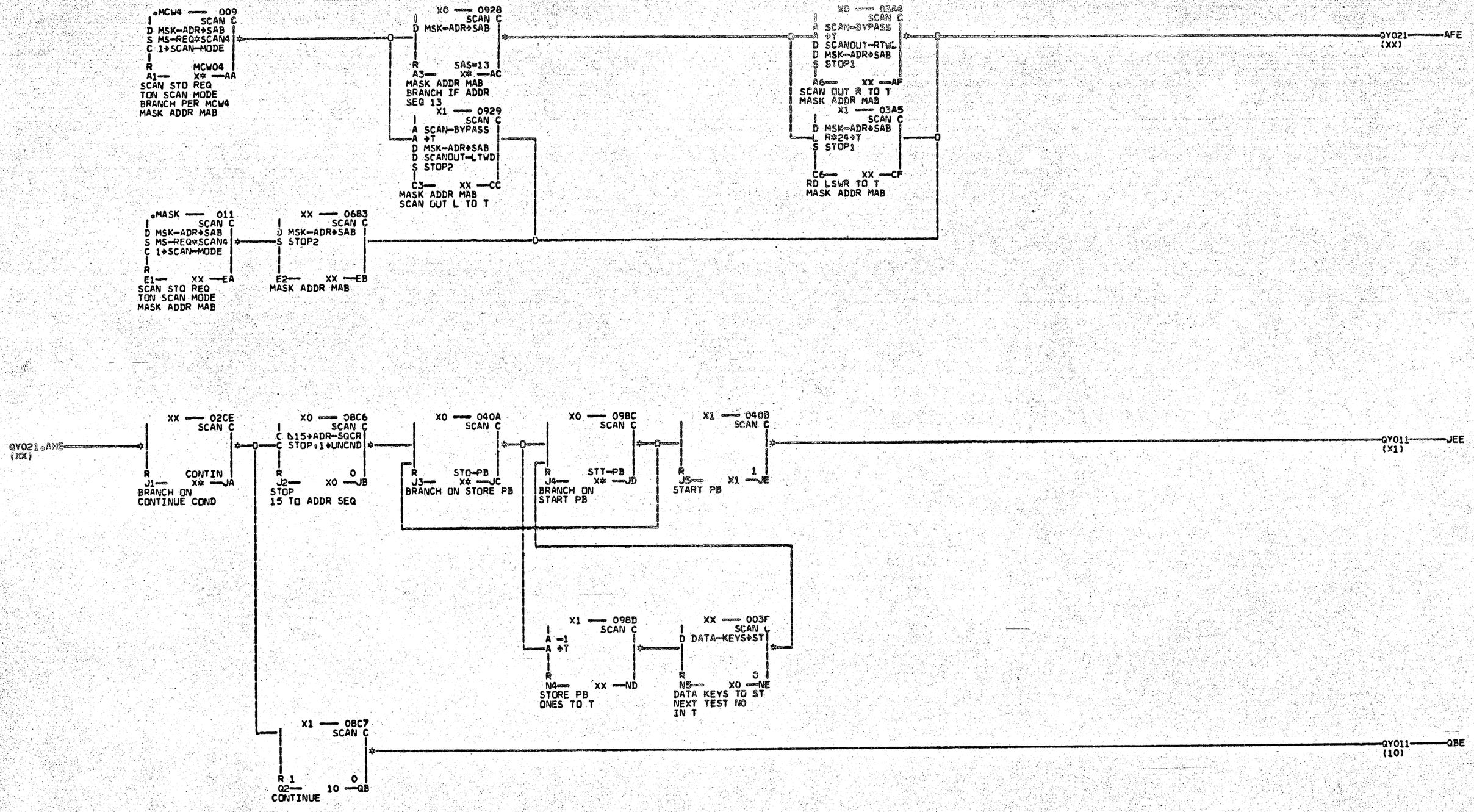


713671	04/28/70	MACH	C7201-02	DATE 05/21/70	SHEET 1 QY015
		NAME		LOG 048	VERSION
		MODE	MANUAL	SCAN IN WORDS 8 THRU 15	
		P.N.	2583898		
		IBM CORP.	SDD		



713671	04/28/70	MACH	C7201-02	DATE	05/21/70	SHEET 1	QY021
		NAME		LOG	048	VERSION	
		MODE	MANUAL	SCAN IN AND RESULT COMPARISON			
		P.N.	2583899	FLT TEST			
		IBM CORP.	SDD				

QY021



713671	04/28/70	MACH	C7201-02	DATE	05/21/70	SHEET	1 QY031
		NAME		LOG	048	VERSION	
		MODE	MANUAL	ERROR ROUTINE TO RESTART			
		P.N.	2583916	FLT TEST			
		IBM CORP.	SDD				

QY031

POWER ON RESET
SET MANUAL TGR.
ONES TO E 8-15 TO ADDRESS LOCAL STORE FOR CLEAR.
CLEAR F.G.K.IC.T AND LOCAL STORE REGISTERS.

.REST — 000B
A -1
A →E
C 1→STOP-LOOP
B →G.F
A1— XX —AA

XX — 002B
A →ΔK
R 0
C1— 01 —CA

C: — 0839
A →IC.T
L W*E11-15
C E23-1
R E23=0 1
A2— *1 —AB
ZEROS TO LS PER E11-15

11 — 083B
A3— XO —AC
GO TO STOP LOOP

XO — 0892
C 0→STAD
R S70=PB
A4— XX —AD
BR IF STORE

X1 — 088F
A -1
A →T.D
L W*24
A3— XX —AE
ONES TO Y AND D REG

XO — 088E
R DIS=PB
C5— X* —CE
BR IF DISPLAY

XX — 0884
A E23-0
A →A
D ADR-KEYS→D
L RW24→S
A6— XX —AF
SAVE E8-15 IN A REG
ADDR TO D REG
ONES TO S REG

XX — 0882
A 0→D
A →E
B -1→0
B →F
D D→STC
D DATA-KEYS→ST
R LS=PB
A7— X* —AG
LOCAL STOR ADDR TO E8-15
ONES TO F REG

XO — 088A
A IC=D+7
A R→
S MS-REG*D=4
C 1→ASC*J57=0
C INH=MS=PROT
R SB=PB
A8— X* —AH
STORE DATE PER D
TEST FOR ASC

X1 — 0889
A A+0
A →E
S SET-MARK*STC
R 1 0
A9— 10 —AI
RESTORE E8-15
STORE ONE BYTE

X1 — 0888
A A+0
A →E
L W*E11-15

R 1 0
C8— 10 —CH
DATA TO LOCAL STORE PER E8-15
RESTORE E8-15

XO — 0888
A A+0
A →E
S SET-MARK=0-7

R 1 0
C9— 10 —CI
RESTORE E8-15
STORE DOUBLE WORD

•STOP — 0003
A IC+0
A →IC.ΔK
C 1→STOP-LOOP
R 0
C3— XO —CC
MACHINE RESET.
SET MANUAL TGR.
GOOD PARITY TO IC.K

X1 — 0893
A IC+0-16
A →D
C4— XO —CD

X1 — 0805
A -1
A →D
C6— XX —CF
ONES TO D REG

XO — 0804
R IC=PB
E6— X* —EF
BR IF SET IC

X1 — 0807
A -1
A →D
C 0→BR-INV-ADR
E7— XX —EG
ONES TO D REG

QQ031.NIE (11) FROM RECEIVE ATR.

XO — 0932
C STC+1
E2— XX —EB

XX — 093B
B O+U
B →ST AB→U
E3— XX —EC
SECOND PART OF SYSTEM MASK TO ST BYTE 2

XX — 09B2
D SO→PSW
C EEOP
E4— XX —ED
SYSTEM MASK TO PSW 0-7.16-19

QJ031.AFE (X0)

QJ031.AHE (XX)

QY051 (XX) —EGE

XO — 0806
R STT=PB
G7— X* —GG
BR IF START

X1 — 088D
C 0→STP.STPLP
G8— XX —GH
TOF MANUAL AND STOP TGRS

XX — 0443
C 0→TIME-GATE
G9— XX —GI
DELAY CYCLE TOF READ DIRECT INTERRUPT IF RDD.

QT041 (XX) —GIE

QJ041.QFE (XX)

QT071.SIE (XX)

QY051.JIE (XX)

XX — 0836
A E23+0
A →A
D ADR-KEYS→D
J1— XX —JA
SAVE E8-15 IN A REG
ADDRESS TO D REG

XX — 08C0
A 0→D
A →E
B -1+0
B →F
R LS=PB
J2— X* —JB
LOCAL STOR ADDR TO E8-15
BR IF LOCAL STOR

XO — 08C2
S MS-REG*D-3
C INH-MS-PROT
J3— XX —JC
FETCH DATA PER ADDR IN D REG

XX — 08BA
A A+0
A →E
D MS→ST MS→AB
S STOP2
R 1 0
J4— 10 —JD
DATA TO ST AB REG
RESTORE E8-15

X1 — 08C3
A A+0
A →E
L R*E11-15→T
R 1 0
L4— 10 —LD
LOCAL STORE DATA PER E8-15 TO T
RESTORE E8-15

XO — 088C
R ROS=PB
J8— X* —JH
BR IF ROS TFR

X1 — 0891
A -1
A →D
J9— XX —JI
ONES TO D REG

QY061 (XX) —JIE

XO — 0890
R ΔREG=SET
L6— X* —LF
BR IF REG SET PB.

XO — 08B8
C 0→ABC
R IPLΔPSW
L7— X* —LG
BR IF PSW RESTART IPL OR EXTERNAL START. ZERO ABC.

X1 — 08AD
R ΔFLT
L8— X* —LH
BRANCH IF FLT OR ROS TEST

XO — 033E
A -1
A →T
L RΔCCR→S
C 3→STC
L9— XX —LI
CONF BITS TO S REG
ONES TO T REG

QY061 (XX) —LIE

QY011 (X1) —LHE

QY051.AIE (10)

•MANT — 0026
B -1+0
B →F
C 1→STOP-LOOP
R 1 0
N2— 10 —NB
TON MANUAL TGR

X1 — 08B9
A -1
A →T.D
L W*24
N7— XX —NG

XO — 08AC
R ΔATRSEL
N8— X* —NH
BR IF RECEIVE ATR

X1 — 08B8
C 1→STAD
C Δ1→INTR-GATE
N9— XX —NI
TN STAT D TO REMEMBER STOP LOOP
SEND RESPONSE

QQ031 (XX) —NIE

XX — 0832
R 1 W=0
Q2— 1* —QB
BR IF F REG = 0

10 — 083A
*B -1+U
B →F F→U
S STOP1
C 0→STAD
Q3— XX —QC
DELAY LOOP

XX — 08BC
L R*24→S
Q5— XX —QE
ONES TO S REG

XX — 08BD
D DATA-KEYS→ST
Q6— XX —QF

XX — 08BE
D ΔT→EXTREG
Q7— XX —QG

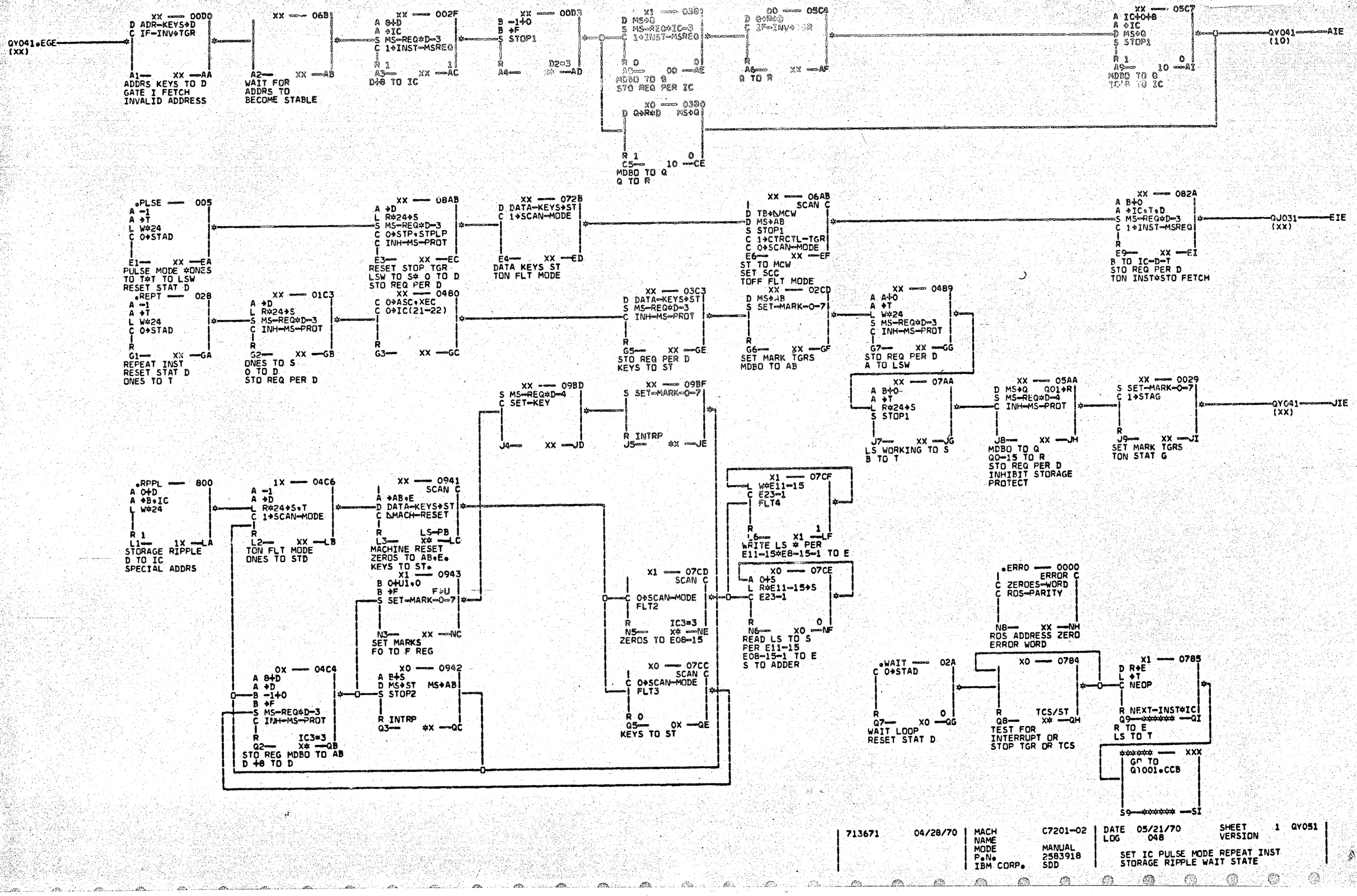
XX — 08BF
B -1+0
B →F
D ΔLOAD-REG
R 1 0
Q8— 10 —QH
ONES TO F REG
LOAD CCR.ATR.
OR PSBAR PER REGISTER SELECT SWITCH

XO — 08BA
A IC+0-8
A →D
R IC3=3
Q9— X* —QI

713671	04/28/70	MACH	C7201-02	DATE	05/21/70	SHEET	1 QY041
		NAME		LOG	048	VERSION	
		MODE	MANUAL				
		P.N.	2583917				
		IBM CORP.	SDD				

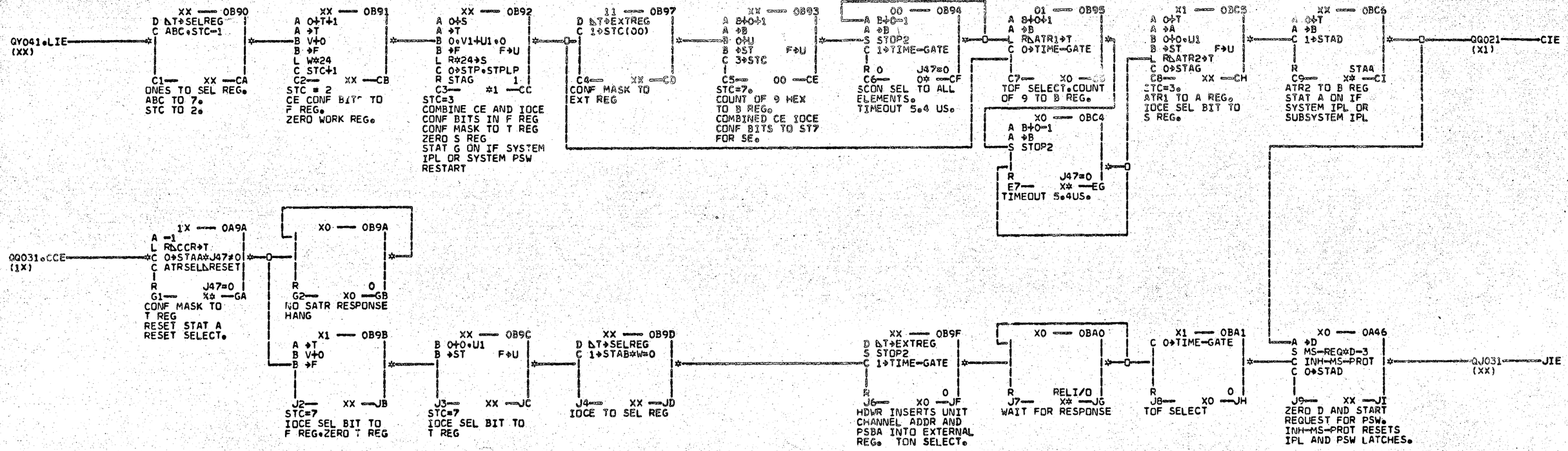
STOP LOOP AND STORE DISPLAY AND LOAD REG MANUAL CONTROLS

4 0 4 0 1



713671	04/28/70	MACH	C7201-02	DATE 05/21/70	SHEET 1 QY051
		NAME		LOG 048	VERSION
		MODE	MANUAL	SET IC PULSE MODE REPEAT INST	
		P•N•	2583918	STORAGE RIPPLE WAIT STATE	
		IBM CORP•	SDD		

QY051



QY061

713671	04/28/70	MACH	C7201-02	DATE 05/21/70	SHEET 1 QY061
		NAME		LOG 048	VERSION
		MODE	MANUAL	SYSTEM • SUBSYSTEM IPL OR PSW	
		P•N•	2583919	RESTART OR EXTERNAL START	
		IBM CORP•	SDD		